

METHOD FOR RECEPTION AND SEARCH OF PACKET- SWITCHED SIGNAL

Bibliographic data

[Description](#) [Claims](#) [Mosaics](#) [Original document](#) [INPADOC legal status](#)

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Inventor(s): NOAM A ZIV [US]; ROBERTO PADOVANI [US]; DZHEFFRI A LEVIN [US]; KENNET D ISTON [US]
Applicant(s): QUALCOMM INC [US]
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- international: H04J13/00; H04B1/707; H04B7/26; H04J13/04; H04B1/707; H04B7/26; H04J13/02; (IPC1-7): H04B7/26
- European: [H04B1/707; H04B1/707A1A; H04B1/707A9](#)
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

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Abstract of **RU 2157592 (C2)**

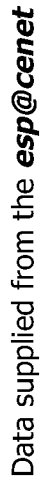
communication equipment. SUBSTANCE: integral search processor, which is used in modem of extended spectrum communication system, provides buffering of samples of received signals in buffer and uses conversion processor, which uses time sampling and operates in sequential steps with respect to buffer. Search processor achieves step-by-step autonomous search, which is configured by microprocessor, which defines set of search parameters, including group of antennas for search, initial shift and search window width, as well as number of Walsh characters for accumulation of results for each shift. Search processor calculates correlation power for each shift and produces final list of best possible signal spreading tracks, which are detected upon search, in order to use them for extension of demodulation elements.; Search is linear and independent from probability of the fact that target signal was sent in any arbitrary time moment. EFFECT: increased functional capabilities. 2 cl, 15 dwg

Also published as:

-  WO9635268 (A1)
-  ZA9603188 (A)
-  MX9708514 (A)
-  JP11505083 (T)
-  IL118116 (A)

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